INTELLIGENT
DISPLAY SOLUTIONS

IDB-REX012864KXX-01.

Overview

IDS have designed a break-out board for the REX012864KXX range of standard COG displays.

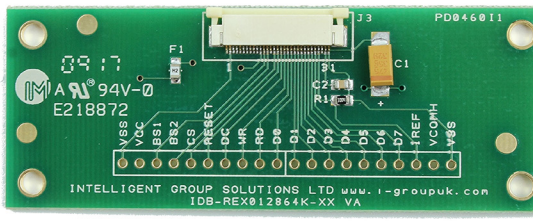
The IDB-REX012864KXX-01. enables the REX012864KXX OLED displays to be used with the industry standard 0.1" header.

We have incorporated all the signals with no driving effect, leaving just the relevant ones for operation.

LCD Connection

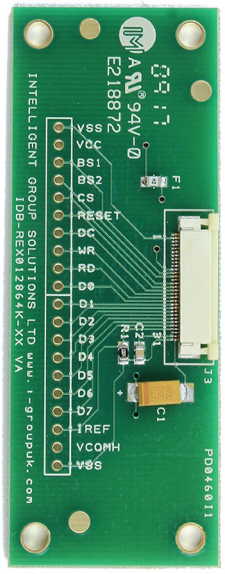
Insert the LCD Flexi with the white side up

Push the brown Flexi Clamp Back in place

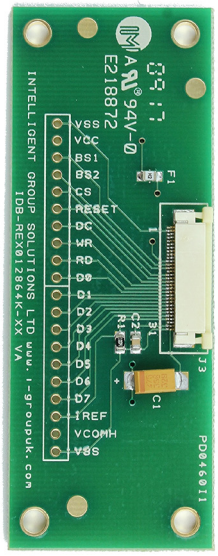


Pin Description

Pin no	Symbol	Function
1	VSS	Ground.
2	VCC	Power supply for panel driving voltage. This is also the most positive power voltage supply pin.
3	BS1	MCU bus interface selection pins. Select appropriate logic setting as described in the following table. BS2, BS1 and BS0 are pin select I2C: BS1 = 1, BS2 = 0 4-wire Serial: BS1 = 0, BS2 = 0 8-bit 68XX Parallel: BS1 = 0, BS2 = 1 8-bit 80XX Parallel: BS1 = 1, BS2 = 1 Note (1) 0 is connected to VSS (2) 1 is connected to VDD
4	BS2	
5	CS	This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW).
6	RESET	This pin is reset signal input. When the pin is pulled LOW, initialization of the chip is executed. Keep this pin pull HIGH during normal operation.



Pin no	Symbol	Function	
7	DC	This pin is the Data/Command control pin connecting to the MCU. When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data. When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register. In I2C mode, this pin acts as SA0 for slave address selection. When 3-wire serial interface is selected, this pin must be connected to VSS.	
8	WR	This pin is read/write control input pin connecting to the MCU interface. When 6800 interface mode is selected, this pin will be used as Read/Write (R/W#) selection input. Read mode will be carried out when this pin is pulled HIGH and write mode when LOW. When 8080 interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.	
9	RD	This pin is MCU interface input. When 6800 interface mode is selected, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled HIGH and the chip is selected. When 8080 interface mode is selected, this pin receives the Read (RD#) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.	
10	D0	These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie LOW. When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SDIN and D2 should be kept NC. When I2C mode is selected, D2, D1 should be tied together and serve as SDAout. SDAin in application and D0 is the serial clock input, SCL.	
11	D1		
12	D2		
13	D3		
14	D4		
15	D5		
16	D6		
17	D7		
18	IREF	This pin is the segment output current reference pin. IREF is supplied externally.	
19	VCOMH	COM signal deselected voltage level. A capacitor should be connected between this pin and VSS.	
20	VSS	Ground.	



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